

<b>Form PTO-1449</b> (modified) U.S. Patent and Trademark Office For Applicant's Information Disclosure Statement (Use several sheets if necessary)	ATTY. DKT. NO. 5500-95500  APPLICANT: Hamilton, et al.  FILING DATE: October 1, 2003	SERIAL NO. 10/676,600  GROUP: 2185
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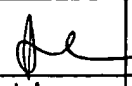

**U.S. PATENT DOCUMENTS**

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE

**FOREIGN PATENT DOCUMENTS**

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO

**OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)**

	A1	AMD, "BIOS and Kernel Developer's Guide for AMD Athlon™ 64 and AMD Operton™ Processors, Publication #26094, Revision 3.06, September 2003, 9 pages.
	A2	AMD, AMD-8111™ HyperTransport™ I/O Hub, Data Sheet, Publication #24674, Revision 3.00, April 2003, pp. 37-134.

EXAMINER:

DATE CONSIDERED:

2/19/08

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.